

FPGA Implementation using by Verilog

REVIEW:

writing code in Verilog, Designing test bench, hardware architecture design technique

Verilog HDL:

Data Types , Modeling Concepts Task and Functions , Specify Block and Timing Checks, Verification and Writing Test Benches

FPGA Architecture:

Architecture study of some popular FPGA families, Detailed study of a Xilinx FPGA family