

Chhattisgarh Swami Vivekanand Technical University, Bhilai, CG

SCHEME OF TEACHING AND EXAMINATION

M. Tech. Electronics & Telecommunication Engineering (Instrumentation & Control)

IV Semester

| S. No. | Board of Studies | CODE | SUBJECT | Periods per week | | | Scheme of Exam. <u>Theory / Practical</u> | | | GRAND TOTAL | Credit $L+(T+P)/2$ |
|--------|-------------------------|------------|--------------|------------------|----------|-----------|--|------------|----------|-------------|-----------------------|
| | | | | L | T | P | ESE | TA | CT | | |
| 1 | Electronics & Telecomm. | 552421(28) | Dissertation | 6 | - | 34 | 300 | 200 | - | 500 | 23 |
| | | | TOTAL | 6 | - | 34 | 300 | 200 | - | 500 | 23 |

L – Lecture, T – Tutorial, P – Practical, ESE- End Semester Exam , CT- Class Test TA – Teacher's Assessment