

***Chhattisgarh Swami Vivekanand Technical University, Bhilai***  
**Scheme of teaching and examination**

**M.Tech. (DIGITAL ELECTRONICS) in the Department of Electronics & Telecommunication**

**IV SEMESTER**

S.No	Board of Study	Subject Code	Subject Name	Periods per week			Scheme of Exam			Total Marks	Credit L+(T+P)/2
				L	T	P	Theory/Practical				
							ESE	CT	TA		
1	Electronic & Telecom..	555421 (28)	PROJECT + Seminar	6	-	34	300	-	200	500	23
<b>Total</b>				<b>6</b>	<b>-</b>	<b>34</b>	<b>300</b>	<b>-</b>	<b>200</b>	<b>500</b>	<b>23</b>

L-Lecture, T- Tutorial, P- Practical, ESE- End Semester Examination, CT - Class Test, TA - Teacher's Assessment

**Scheme of marks Allotment**

Semester	Total Marks	Grand Total
I	1000	3000
II	1000	
III	500	
IV	500	