

Chhattisgarh Swami Vivekanand Technical University, Bhilai

M.E. Electronics & Telecommunication (Specialization in VLSI Design)

FOURTH SEMESTER

| Sr. No. | Board of Study | Subject Code | Subject | Periods Per Week | | | Scheme of Examination | | | Total Marks | Credit L+(T+P)/2 |
|--------------|-----------------------|--------------|------------------------------|------------------|----------|-----------|-----------------------|----------|------------|-------------|------------------|
| | | | | | | | Theory / Practical | | | | |
| | | | | L | T | P | ESE | CT | TA | | |
| 1 | Electronics & Telecom | 560421 (28) | Project + Seminar on Project | 6 | - | 34 | 300 | - | 200 | 500 | 23 |
| TOTAL | | | | 6 | - | 34 | 300 | - | 200 | 500 | 23 |

L - Lecture
P - Practical
CT - Class Test

T - Tutorial
ESC – End Semester Exam
TA – Teachers Assessment

Scheme of Marks Allotment

| Semester | Total Marks | Grand Total |
|----------|-------------|-------------|
| I | 1000 | 3000 |
| II | 1000 | |
| III | 500 | |
| IV | 500 | |